Electronic Acknowledgement Receipt FES ID: 1018489 Application Number: 10595303 Confirmation Number: 7114 Method and Device For Secure, Insulated and Electrically Conductive Title of Invention: Assembling of Treated Semiconductor Wafers First Named Inventor: Roy KNETCHTEL Customer Number: 21967 Filer: Rodger L. Tate/Mildred Ayim Filer Authorized By: Rodger L. Tate Attorney Docket Number: 60291.000048 Receipt Date: 06-APR-2006 Filing Date: Time Stamp: 14:55:19 Application Type: U.S. National Stage under 35 USC 371 International Application Number: PCT/DE04/02413

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